Cache related pre-emption delay aware response time analysis for fixed priority pre-emptive systems

Sebastian Altmeyer  
Compiler Design Lab  
Saarland University, Germany  
Email: altmeyer@cs.uni-saarland.de

Robert I. Davis  
University of York  
York, UK  
Email: rob.davis@cs.york.ac.uk

Claire Maiza  
Verimag  
INP Grenoble, France  
Email: Claire.Maiza@imag.fr

Abstract—Without the use of cache the increasing gap between processor and memory speeds in modern embedded microprocessors would have resulted in memory access times becoming an unacceptable bottleneck. In such systems, cache related pre-emption delays can be a significant proportion of task execution times. To obtain tight bounds on the response times of tasks in pre-emptively scheduled systems, it is necessary to integrate worst-case execution time analysis and schedulability analysis via the use of an appropriate model of pre-emption costs.

In this paper, we introduce a new method of bounding pre-emption costs, called the ECB-Union approach. The ECB-Union approach complements an existing UCB-Union approach. We combine the two into a simple composite approach that dominates both. These approaches are integrated into response time analysis for fixed priority pre-emptively scheduled systems. Further, we extend this analysis to systems where tasks can access resources in mutual exclusion, in the process resolving omissions in existing models of pre-emption delays. A case study and empirical evaluation demonstrate the effectiveness of the ECB-Union and combined approaches for a wide range of different cache configurations including cache utilization, cache set size, reuse, and block reload times.

I. INTRODUCTION

During the last two decades, applications in aerospace and automotive electronics have progressed from deploying embedded microprocessors clocked in the 10's of MHz range to significantly higher performance devices operating in the high 100's of MHz to GHz range. The use of such high performance embedded processors has meant that memory access times have become a significant bottleneck, necessitating the use of cache to tackle the increasing gap between processor and memory speeds.

In the majority of research papers on fixed priority pre-emptive scheduling an assumption is made that the costs of pre-emption can either be neglected or sub-summed into the worst-case execution time of each task. With today's high performance embedded processors, pre-emption costs can make up a significant proportion of each task's execution time. Such costs cannot be neglected nor is it necessarily viable to simply subsume them into worst-case execution times, as this can lead to a pessimistic overestimation of response times.

In this paper, we consider the costs incurred when a pre-empting task evicts useful cache blocks of a pre-empted task. These useful cache blocks subsequently need to be reloaded after the pre-empted task resumes execution, introducing an additional cache related pre-emption delay (CRPD).

Non-pre-emptive scheduling is one way of avoiding such cache-related pre-emption costs; however, disabling pre-emption is often not an option. Systems that include tasks or interrupt handlers with short deadlines typically cannot disable pre-emption for the full duration of each task's execution. An alternative approach is co-operative scheduling, with re-scheduling only possible at specific pre-emption points within each task, or after a pre-determined time has elapsed, thus dividing each task into a series of non-pre-emptable sections. Recently, significant progress has been made in this area, with algorithms designed to make an optimal selection of pre-emption points [9, 10]. These algorithms minimise the overall cost of pre-emption for each task while maintaining the schedulability of the taskset as a whole. However, difficulties remain, for example in determining the placement of pre-emption points when the code includes branches and loops.

Exact response time analysis for fixed priority pre-emptive systems was developed during the 1980's and 1990's and subsequently refined into a set of engineering techniques [16, 6, 15]. However, basic response time analysis does not consider cache-related pre-emption costs explicitly. Explicit integration of pre-emption costs has previously been considered in a number of ways: analyzing the effect of the pre-empting task [13, 27], the effect on the pre-empted task [18], or a combination of both [25, 26]. With later refinements giving an upper bound on the number of pre-emptions [22].

In fixed priority pre-emptive systems, there are a number of ways of managing task priorities that can be used to reduce the number of pre-emptions and hence the overall pre-emption costs. These include: non-pre-emption groups [14], pre-emption thresholds [17, 23, 28], and FP-FIFO scheduling [20], which is supported by a large number of real-time operating systems, including the Linux kernel (SCHED_FIFO).

In this paper, we build upon previous work that integrates pre-emption costs into response time analysis for fixed priority pre-emptive scheduling. Section II introduces the scheduling model, terminology, and notation used. In Section III, we
review existing approaches to integrating pre-emption costs into response time analysis. Building on the insights gained from this review, Section IV introduces the new ECB-Union approach to computing pre-emption costs. The ECB-Union approach complements an existing UCB-Union approach. We combine the two into a simple composite that dominates both. In Section V, we extend our analysis to systems where tasks can access resources in mutual exclusion, in the process resolving omissions in existing models of pre-emption delays. A case study in Section VI and an empirical evaluation in Section VII demonstrate the effectiveness of the ECB-Union and combined approaches for a wide range of different task parameters and cache configurations. Section VIII concludes with a summary of the main contributions of the paper.

The research in this paper focuses on fixed priority pre-emptive scheduling with unique priority levels; however, the approaches derived are also applicable to FP-FIFO scheduling. Extension to FP-FIFO scheduling is described in the Appendix of a technical report [3] which forms an extended version of this paper.

II. TASK MODEL, TERMINOLOGY, AND NOTATION

We are interested in an application executing under a fixed priority pre-emptive scheduler on a single processor. The application is assumed to comprise a static set of $n$ tasks ($\tau_1, \tau_2, \ldots, \tau_n$), each assigned a fixed priority. We use the notation $lp(i)$ (and $lp(j)$) to mean the set of tasks with priorities higher than (lower than) that of $\tau_i$. Similarly, we use the notation $hp(i)$ (and $hp(j)$) to mean the set of tasks with priorities higher than or equal to (lower than or equal to) that of $\tau_i$. We consider systems where each task has a unique priority.

Application tasks may arrive either periodically at fixed intervals of time, or sporadically after some minimum inter-arrival time has elapsed. Each task, is characterized by: its relative deadline $D_i$, worst-case execution time $C_i$, minimum inter-arrival time or period $T_i$ and release jitter $J_i$, defined as the maximum time between the task arriving and it being released (ready to execute). Tasks are assumed to have constrained deadlines, i.e. $D_i \leq T_i$. It is assumed that once a task starts to execute it will never voluntarily suspend itself. The processor utilization $U_i$ of task $\tau_i$ is given by $C_i/T_i$. The total utilization $U$ of a taskset is the sum of the individual task utilizations. The worst-case response time $R_i$ of a task $\tau_i$, is the longest time from it becoming ready to execute to it completing execution. A task is referred to as schedulable if its worst-case response time is less than or equal to its deadline less release jitter ($R_i \leq D_i - J_i$). A taskset is referred to as schedulable if all of its tasks are schedulable.

In Section III and Section IV we assume that tasks are independent. In Section V, we relax this restriction, permitting tasks to access shared resources in mutual exclusion according to the Stack Resource Policy (SRP) [7]. As a result of the operation of the SRF, a task $\tau_i$ may be blocked by lower priority tasks for at most $B_i$, referred to as the blocking time.

In our analysis of cache related pre-emption delays, we use aff($i, j$) to mean the set of tasks that can not only execute between the release and completion of task $\tau_i$ and so affect its response time, but can also be pre-empted by task $\tau_j$. For the basic task model, without shared resources, aff($i, j$) = heap($i$) $\cap$ lp($j$).

Pre-emption Costs

We now extend the sporadic task model introduced above to include pre-emption costs. To this end, we need to explain how pre-emption costs can be derived. To simplify the following explanation and examples, we assume direct-mapped caches.

The additional execution time due to pre-emption is mainly caused by cache evictions: the pre-empting task evicts cache blocks of the pre-empted task that have to be reloaded after the pre-empted task resumes. The additional context switch costs due to the scheduler invocation and a possible pipeline-flush can be upper-bounded by a constant. We assume that these constant costs are already included in $C_i$. Hence, from here on, we use pre-emption cost to refer only to the cost of additional cache reloads due to pre-emption. This cache-related pre-emption delay (CRPD) is bounded by $g \times BRT$ where $g$ is an upper bound on the number of cache block reloads due to pre-emption and $BRT$ is an upper-bound on the time necessary to reload a memory block in the cache (block reload time).

To analyse the effect of pre-emption on a pre-empted task, Lee et al. [18] introduced the concept of a useful cache block: A memory block $m$ is called a useful cache block (UCB) at program point $P$, if (i) $m$ may be cached at $P$ and (ii) $m$ may be reused at program point $Q$ that may be reached from $P$ without eviction of $m$ on this path. In the case of pre-emption at program point $P$, only the memory blocks that (i) are cached and (ii) will be reused, may cause additional reloads. Hence, the number of UCBs at program point $P$ gives an upper bound on the number of additional reloads due to pre-emption at $P$. The maximum possible pre-emption cost for a task is determined by the program point with the highest number of UCBs. Note that for each subsequent pre-emption, the program point with next smaller number of UCBs can be considered. Thus, the $j$-th highest number of UCBs can be counted for the $j$-th pre-emption. A tighter definition is presented in [1]; however, in this paper we need only the basic concept.

The worst-case impact of a pre-empting task is given by the number of cache blocks that the task may evict during its execution. Recall that we consider direct-mapped caches: in this case, loading one block into the cache may result in the eviction of at most one cache block. A memory block accessed during the execution of a pre-empting task is referred to as an evicting cache block (ECB). Accessing an ECB may evict a cache block of a pre-empted task.

In this paper, we represent the sets of ECBs and UCBs as sets of integers with the following meaning:

$s \in UCB_i \iff \tau_i$ has a useful cache block in cache-set $s$

$s \in ECB_i \iff \tau_i$ may evict a cache block in cache-set $s$
A bound on the pre-emption cost due to task \( \tau_j \) directly pre-empting \( \tau_i \) is therefore given by \( \text{BRT} \cdot |\text{UCB}_i \cap \text{ECB}_j| \). Precise computation is more complex as different program points may exhibit different sets of UCBs. Hence, the worst-case pre-emption delay considering a pre-empting and pre-empted task may not necessarily occur at the pre-emption point with the highest number of UCBs but instead at the point with the largest intersection between UCBs and ECBs—see [4] for a detailed description of the computation of pre-emption costs. Note that the simplification we apply, using ECB, and UCB, does not impact the correctness of the equations.

Separate computation of the pre-emption cost is restricted to architectures without timing anomalies [19] but is independent of the type of cache used, i.e., data, instruction or unified cache.

Set-Associative Caches: In the case of set-associative LRU caches\(^1\), a single cache-set may contain several useful cache blocks. For instance, \( \text{UCB}_1 = \{1, 2, 2, 3, 4\} \) means that task \( \tau_1 \) contains 3 UCBs in cache-set 2 and one UCB in each of the cache sets 1, 3, and 4. As one ECB suffices to evict all UCBs of the same cache-set, multiple accesses to the same set by the pre-empting task does not need to appear in the set of ECBs. Hence, we keep the set of ECBs as used for direct-mapped caches. A bound on the CRPD in the case of LRU caches due to task \( \tau_j \) directly pre-empting \( \tau_i \) is thus given by the intersection \( \text{UCB}_j \cap \text{ECB}_i = \{m | m \in \text{UCB}_j \land m \in \text{ECB}_i\} \), where the result is also a multiset that contains each element from \( \text{UCB}_i \) if it is also in \( \text{ECB}_j \). A precise computation of the CRPD in the case of LRU caches is given in [5]. In this paper, we assume direct-mapped caches. Note that all equations provided within this paper are for direct-mapped caches, they are also valid for set-associative LRU caches with the above adaptation to the set-intersection.

III. RESPONSE TIME ANALYSIS FOR PRE-EMPTIVE SYSTEMS

Response time analysis [6, 16] for fixed priority pre-emptive scheduling calculates the worst-case response time \( R_i \) of task \( \tau_i \), using the following equation:

\[
R_i = C_i + B_i + \sum_{j \in \text{set}(i)} \left( \frac{R_j + J_j}{T_j} \right) (C_j)
\]  

(1)

Note that the worst-case response time appears on both the left-hand side (LHS) and the right-hand side (RHS) of the equation. As the RHS is a monotonically non-decreasing function of \( R_i \), the equation can be solved using fixed point iteration. Iteration starts with an initial value for the response time, typically \( R_0^i = C_i + B_i \), and ends either when \( R_k = R_{k+1} \) in which case the worst-case response time \( R_i \) is given by \( R_k \) or when \( R_i > D_i - J_i \) in which case the task is unschedulable. We note that (1) does not explicitly include pre-emption costs.

A. Existing Analyses including pre-emption costs

Equation (1) can be extended by \( \gamma_{i,j} \) representing the pre-emption cost due to each job of a higher priority pre-empting task \( \tau_j \) executing within the worst-case response time of task \( \tau_i \) [13]:

\[
R_i = C_i + \sum_{j \in \text{set}(i)} \left( \frac{R_j + J_j}{T_j} \right) (C_j + \gamma_{i,j})
\]  

(2)

Note that task \( \tau_j \) does not necessarily pre-empt task \( \tau_i \) directly; a nested pre-emption is also possible. Any pre-emption by task \( \tau_j \) of a task \( \tau_k \) that executes while \( \tau_i \) is pre-empted may also increase the response time of task \( \tau_i \). The problem of obtaining a valid yet tight upper bound on the pre-emption costs is made difficult by the effects of nested pre-emption, as a pre-empting task may evict useful cache-blocks belonging to a number of pre-empted tasks.

The precise meaning of \( \gamma_{i,j} \) and its computation depends on the approach used. Below, we review a number of existing approaches and discuss their advantages and disadvantages.

ECB-Only

Busquets and Wellings [13] and later Tomiyama and Dutt [27] used the ECBs of the pre-empting task to bound the pre-emption costs:

\[
\gamma_{i,j}^{\text{ecb}} = \text{BRT} \cdot |\text{ECB}_j|
\]  

(3)

In this case, \( \gamma_{i,j} \) represents the worst-case effect of task \( \tau_j \) on any arbitrary lower priority task, independent of such a task’s actual UCBs.

UCB-Only

By contrast, Lee et al. [18] used the number of UCBs to bound the pre-emption costs. Here, however one has to account for nested pre-emptions. The cost of \( \tau_j \) pre-empting some task \( \tau_k \) of intermediate priority may be higher than that of \( \tau_j \) pre-empting \( \tau_i \). Thus, the pre-emption cost due to a job of task \( \tau_j \) executing during the response time of task \( \tau_i \) is only bounded by the maximum number of UCBs over all tasks that may be pre-empted by \( \tau_j \) and have at least the priority of \( \tau_i \) (i.e., the tasks from the set \( \text{aff}(i,j) = \text{hep}(i) \cap \text{lp}(j) \)).

\[
\gamma_{i,j}^{\text{ucb}} = \text{BRT} \cdot \max_{k \in \text{aff}(i,j)} |\text{UCB}_k|
\]  

(4)

The disadvantage of the ECB-Only and UCB-Only approaches is clear: considering only the pre-empted tasks or alternatively only the pre-empting tasks leads to an over-approximation. Not every ECB may be evicted during pre-emption, and not every UCB may evict a UCB. This is illustrated in Figure 1.

Figure 1 shows an example taskset that leads to an over-approximation when the pre-emption cost is estimated using (3) or (4). Task \( \tau_1 \) accesses blocks in cache sets 1 and 2. Task \( \tau_2 \) accesses blocks in cache sets 2, 3, and 4. However, only sets 3 and 4 may contain useful cache blocks; hence a pre-emption of task \( \tau_2 \) by task \( \tau_1 \) never evicts any useful cache blocks; and so there are no cache reloads due to pre-emption. However, (4) and (3) account for 2 additional reloads; an overestimation of the pre-emption cost.

Since both (3) and (4) can over-estimate the actual pre-emption cost, combining both UCBs and ECBs might be

\[ \]
expected to result in precise bounds. However, the naive
computation $\gamma_{i,j} = BRT \cdot [UCB_i \cap ECB_j]$ is optimistic and
thus cannot be used. It may lead to underestimation in two
cases: when the cost of task $t_j$ pre-empting a task $t_k$ of
intermediate priority is higher than that of $t_j$ pre-empting $t_i$
(see Figure 2(a)) and when the execution of $t_j$ may evict useful
blocks of both task $t_i$ and of task $t_k$ (see Figure 2(b)).

UCB-Union
Tan and Mooney [26] considered both the pre-empted and
the pre-empting task. They take the union of all possible
affected useful cache blocks and combine this with the set of
ECBs of the pre-empting task:

$$\gamma_{i,j}^{\text{tan}} = BRT \cdot \left( \bigcup_{v \in \text{blocks}(i)} \text{UCB}_v \right) \cap \text{ECB}_j$$

This UCB-Union approach dominates the ECB-only approach
since:

$$\gamma_{i,j}^{\text{ecb}} \geq \gamma_{i,j}^{\text{tan}}$$

but may be worse than the UCB-only approach in some cases.
For example, consider the taskset shown in Figure 3, the values
of $\gamma_{i,j}$ for the response time analysis of task $t_3$ are as follows:

$$\gamma_{3,1}^{\text{tan}} = \| (\text{UCB}_2 \cup \text{UCB}_3) \cap \text{ECB}_1 \| = \| [1, 2, 3, 4] \cap [1, 2, 3, 4] \| = 4$$

$$\gamma_{3,2}^{\text{tan}} = \| (\text{UCB}_3) \cap \text{ECB}_2 \| = \| [3, 4] \cap [1, 2, 3, 4] \| = 2$$

Given that each task is executed at most once, the total
computed pre-emption cost is 6. However, the actual pre-
emption cost is only 4: either UCBs in cache sets $[1, 2, 3, 4]$
have to be reloaded (in the case of nested pre-emption) or
UCBs in cache sets $[3, 4]$ are reloaded twice (in the case of
consecutive pre-emption of $t_3$ by $t_1$ and then by $t_2$).

$$\gamma_{i,j}^{\text{tan}} = BRT \cdot \left( \bigcup_{v \in \text{blocks}(i)} \text{UCB}_v \right) \cap \text{ECB}_j$$

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consecutive pre-emption of $t_3$ by $t_1$ and then by $t_2$).

Staschulat’s Formula
Staschulat et al. [25] also combine information about
the pre-empting and the pre-empted task; however, their approach
is somewhat more complex than the methods described so
far. Below we give a concise description of their method, for
further details and a more complete description see [25].

The analysis of Staschulat et al. is extended to account for
the fact that each additional pre-emption of task $t_i$ may result
in a smaller pre-emption cost than the last. (Their approach
is an improvement over that of Petters and Färber [21].) The
basic response time analysis used differs from (2): $\gamma_{i,j}$ does
do not refer to the cost of a single pre-emption, but instead to
the total cost of all pre-emption due to jobs of task $t_j$ executing
within the response time of task $t_i$.

$$R_i = C_j + \sum_{v \in \text{blocks}(i)} \left( \frac{R_j}{T_j} \right) \left( C_j + \gamma_{i,j}^{\text{tan}} \right)$$

Staschulat et al. compute the maximum number of
pre-emption $q$, including nested pre-emption, which may impact
the response time of task $t_i$ due to cache blocks evicted by
task $t_j$. Thus $q$ is given by the sum of the maximum number
of jobs of task $t_j$ and tasks of lower priority than $t_j$ but higher
priority than $t_i$ that can execute during the response time $R_i$
of task $t_i$.

$$q = \sum_{v \in \text{blocks}(i)} F_k(R_i)$$
where \( E_k(R_k) \) is used to denote the maximum number of jobs of task \( t_k \) that can execute during response time \( R_k \). For our task model, \( E_k(R_k) = \lfloor (R_k + J_k)/T_k \rfloor \). The total pre-emption cost \( \gamma_{i,j}^{\text{sta}} \) due to jobs of task \( t_j \) pre-empting during the response time of task \( t_i \) is then bounded by the \( q \) largest costs of task \( t_j \) pre-empting jobs of any lower priority task \( t_k \in \text{hep}(i) \cap \text{lp}(j) \) that can execute during the response time of task \( t_i \). As each job of such a task \( t_k \) may execute up to \( E_k(R_k) \) times during \( R_k \), and each of those jobs could potentially be pre-empted at most \( E_k(R_k) \) times by task \( t_j \), the \( E_j(R_k) \) highest pre-emption costs of \( t_j \) directly pre-empting \( t_i \) must be considered \( E_k(R_k) \) times:

\[
\gamma_{i,j}^{\text{sta}} = \text{BRT} \cdot \sum_{k=1}^{n} |M'|
\]

where \( M' \) is the \( l \)-th largest element from the multiset \( M \)

\[
M = \bigcup_{k \in \text{hep}(i) \cap \text{lp}(j)} \bigcup_{t_k \in E_k(R_k)} \left\{ \text{UCB}_k \cap \text{ECB}_j \right\} n \in \{1, E_k(R_k)\}
\]

(8)

and (\( \text{UCB}_k \cap \text{ECB}_j \))\( n \) gives the \( n \)-th highest pre-emption cost for task \( t_j \) pre-empting task \( t_k \). Note that \( M \) is a multiset and the union over \( E_k(R_k) \) means that the set of values for \( t_k \) are repeated \( E_k(R_k) \) times.

The drawback of this approach is that the number of pre-emptions taken into account strongly over-estimates the number of pre-emptions that have an actual influence on the response time; particularly when there are a large number of tasks. In addition, the reduction in the pre-emption costs for a sequence of pre-emptions is typically rather limited ([10] shows that the maximal pre-emption cost can occur at various program points within a task’s execution). The program point \( \mathcal{P} \) in a task which exhibits the highest number of UCBs often occurs within a loop, thus, it has to be taken into account as often as the loop iterates. In addition, program points close to \( \mathcal{P} \) will often have a similar number of UCBs. We note that Staschulat et al. also present an improvement to their analysis in [25]; however, the problem of strongly over-estimating the number of pre-emptions remains.

IV. ECB-Union Approach

We now introduce a new ECB-Union approach to computing pre-emption costs. To account for nested pre-emptions, we compute the union of all UCBs that may affect a pre-empted task. The intuition here is that direct pre-emption by task \( t_j \) is represented by the pessimistic assumption that task \( t_j \) has itself already been pre-empted by all of the tasks of higher priority and hence may result in eviction of \( \bigcup_{k \in \text{hep}(i) \cap \text{lp}(j)} \text{ECB}_k \).

\[
\gamma_{i,j}^{\text{new}} = \text{BRT} \cdot \max_{\mathcal{P} \in \text{aff}(i,j)} \left\{ \text{UCB}_j \cap \left( \bigcup_{k \in \text{hep}(i) \cap \text{lp}(j)} \text{ECB}_k \right) \right\}
\]

(9)

Task \( t_j \) may directly pre-empt any task \( t_k \in \text{aff}(i,j) \) impacting the response time of task \( t_i \). Thus taking the maximum over all of the tasks in \( \text{aff}(i,j) \) ensures that the pre-emption cost for the highest number of evicted useful cache blocks is considered. Note we use \( \text{hep}(j) \cup \{j\} \) to mean task \( t_j \) and all tasks of higher priority than task \( t_j \), rather than \( \text{hep}(j) \). This is because the two sets are different in the more general case where tasks can have priority levels, see [3] for further details. Note that (10) is combined with (2) to determine task response times.

The ECB-Union approach (10) dominates the UCB-only approach, since:

\[
\gamma_{i,j}^{\text{ucb}} \geq \gamma_{i,j}^{\text{new}}
\]

The ECB-Union approach is incomparable with the UCB-Union approach [26]. Figure 3 provides an example where the ECB-Union approach outperforms the UCB-Union approach: here the ECB-Union approach covers both a nested pre-emption (\( t_j \) pre-empted by \( t_k \) which is pre-empted by \( t_i \)) and consecutive pre-emption (of \( t_j \) by \( t_1 \) and \( t_2 \)), obtaining for each pre-emption a cost of 2 and thus, a total cost of 4. In contrast, the UCB-Union approach gives a total cost of 6.

\[
\gamma_{i,j}^{\text{new}} = \max_{\mathcal{P} \in \text{aff}(i,j)} \left\{ \text{UCB}_j \cap \text{ECB}_k \right\}
\]

(9)

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\]

(9)

With the ECB-Union approach, the eviction of UCBs of task \( t_j \) (\( \{1, 2\} \)) is considered twice, even though they must be reloaded at most once, leading to an over-estimation of the total pre-emption costs of 6. The ECB-Union approach, in this case, computes the precise total of 4.

A. Combined Approach

The ECB-Union approach dominates the ECB-Only approach, similarly the ECB-Union approach dominates the UCB-Only approach. Given that the UCB-Union approach (5) and the ECB-Union approach (10) are incomparable, we can combine both to deliver a more precise bound on task response times that, by construction, dominates the use of either approach alone:

\[
R_i = \min(R_i^{\text{lan}}, R_i^{\text{new}})
\]

(11)
where $R_{i}^{\text{old}}$ is the response time of task $\tau_i$ computed using (5) and $R_{i}^{\text{new}}$ is the response time of task $\tau_i$ computed using (10). Note that some tasks are deemed schedulable by the Combined approach but neither by UCB-Union nor ECB-Union. Figure 5 illustrates these relationships.

Fig. 5. Venn Diagram illustrating the relation between the different pre-emption cost aware schedulability tests. The larger the area, the more tasks deemed schedulable.

V. BLOCKING TIME

The discussion in Section III and Section IV assumes non-blocking execution, i.e. no shared resources. In this section, we relax this restriction, permitting tasks to access mutually exclusive shared resources according to the Stack Resource Policy (SRP) introduced by Baker [7], extending the Priority Ceiling Protocol of Sha et al. [24].

The SRP associates a ceiling priority with each resource. This ceiling priority is equal to the highest priority of any task that can access the resource. At run-time, when a task accesses a resource, its priority is immediately increased to the ceiling priority of the resource. Thus SRP bounds the amount of blocking $B_i$ which task $\tau_i$ is subject to, to the maximum time for which any lower priority task holds a resource that is shared with task $\tau_i$ or any other task of equal or higher priority. SRP ensures that a task can only ever be blocked prior to actually starting to execute.

We note that when a lower priority task $\tau_j$ locks a resource and so blocks task $\tau_i$, it can still be pre-empted by tasks with priorities higher than that of the ceiling priority of the resource. $B_i$ does not account for the additional pre-emption cost due to such pre-emption.

Previous work integrating pre-emption costs into response time analysis [13, 18, 25, 26] extends Equation (2) to include blocking via the simple addition of the blocking factor $B_i$:

$$R_i = C_i + B_i + \sum_{j \in \text{hp}(i)} \left[ \frac{R_j + J_j}{T_j} \right] (C_j + \gamma_{i,j}) \tag{12}$$

In the case of Busquets and Wellings analysis [13], this is correct, as the pre-emption cost is accounted for only via the ECBs of the pre-empting tasks and is therefore unaltered by the addition of resource accesses that could potentially also be pre-empted. In contrast, [18, 25, 26] make use of the UCBs of pre-empted tasks. If, as is the case with the SRP, pre-emption can still occur during resource access, then these analyses are optimistic and need to be modified to correctly account for the additional pre-emption costs that can occur. The key point is that the blocking factor $B$ does not represent execution of task $\tau_j$, but instead represents execution of some resource access within a lower priority task. Such a resource access may be pre-empted, during the response time of task $\tau_i$ and therefore its UCBs need to be taken into account, as illustrated by the example in Figure 6.

Fig. 6. Tasks $\tau_2$ and $\tau_3$ share a common resource $x$, $\tau_3$ starts to execute, blocks $\tau_2$, which is released at time 1, and is pre-empted by $\tau_1$. Thus, the finishing time of $\tau_3$ is delayed not only by the time for which $\tau_3$ accesses the resource, but also by the additional pre-emption delay, reloading UCBs $(1,2)$ after the pre-emption of the resource access of task $\tau_3$ by task $\tau_1$.

We now extend the ECB-Union and UCB-Union approaches to take account of blocking. Specifically, we extend the pre-emption cost equations (10) and (5) to include the UCBs of tasks in the set $b(i, j)$, where $b(i, j)$ is defined as the set of tasks with priorities lower than that of task $\tau_i$ that lock a resource with a ceiling priority higher than or equal to the priority of task $\tau_i$, but lower than that of task $\tau_j$ ($b(2,1) = 3$, for the example in Figure 6). These tasks can block task $\tau_i$, but can also be pre-empted by task $\tau_j$. Hence they need to be included in the set of tasks $\text{aff}(i, j)$ whose UCBs are considered when determining the pre-emption cost $\gamma_{i,j}$ due to task $\tau_j$:

$$\text{aff}(i, j) = (\text{hep}(j) \cap \text{lp}(j)) \cup b(i, j) \tag{13}$$

Note that the tasks in $b(i, j)$ have lower priorities than task $\tau_i$ and so cannot pre-empt during the response time of task $\tau_i$, hence their ECBs do not need to be considered when computing $\gamma_{i,j}$. Using (13) extends the ECB-Union approach (10) and the UCB-Union approach (5) to correctly account for
pre-emption costs when tasks share resources according to the SRP.

Revisiting the example given in Figure 6, we observe that as the set of affected tasks aff(2, 1) now includes task t3 as well as task t2, (5) correctly accounts for the overall pre-emption cost of 2 due to the resource access of task t3 being pre-empted by task t1 during the response time of task t2.

We note that in the simplest case of the SRP where tasks share resources that are accessed non-pre-emptively (i.e. with ceiling priorities equal to that of the highest priority task), then the set of tasks aff(i, j) is empty (since no task can pre-empt during a resource access) and hence the pre-emption cost c_j is the same as for the basic task model, with no increase in pre-emption costs due to blocking.

Although providing valid upper bounds on the pre-emption costs, the above extension can be pessimistic. This is because it includes the UCBs of each lower priority task in aff(i, j), rather than just the UCBs of each resource access within those tasks. More precise analysis can be obtained by considering each resource access as a sub-task with its own UCBs, see [3] for further details.

When determining the blocking factor B_i, we cannot use the resource access execution times as they occur within the non-pre-emptive execution of each containing task t_k. This is because we must assume that task t_2 could be pre-empted immediately before a resource access and any useful cache blocks evicted. Instead, the execution time of each resource access must be determined assuming execution of that section of code with no pre-emption, and starting from the worst-case initial state.

VI. CASE STUDY

In this section, we evaluate the effectiveness of the different approaches based on a case study. The worst-case execution times and the set of useful cache blocks and evicting cache blocks have been derived from the Mälardalen benchmark suite, see Table I, where the values are taken from [4]. The target architecture is an ARM7 processor with direct-mapped instruction cache of size 2KB with a line size of 8 Bytes (and thus, 256 cache sets) and a block reload time of 8μs. The ARM7 features an instruction size of 4 Bytes.

We note that although the case study tasks do not represent a set of tasks scheduled on an embedded real-time system, they do represent typical components of real-time applications and thus deliver meaningful values. We created a taskset from the above data by assigning periods and implicit deadlines such that all 15 tasks had equal utilisation. The periods where generated by multiplying each execution time by a constant c (T_i = c \cdot C_i). We varied c from 15 upwards hence varying the utilization of the taskset from 1.0 downwards. The tasks were assigned priorities in descending monotonic priority order.

<table>
<thead>
<tr>
<th></th>
<th>WCET</th>
<th>UCBs</th>
<th>ECBs</th>
</tr>
</thead>
<tbody>
<tr>
<td>bs</td>
<td>445</td>
<td>5</td>
<td>35</td>
</tr>
<tr>
<td>minmax</td>
<td>504</td>
<td>9</td>
<td>79</td>
</tr>
<tr>
<td>fac</td>
<td>1252</td>
<td>4</td>
<td>24</td>
</tr>
<tr>
<td>fibcall</td>
<td>1351</td>
<td>5</td>
<td>24</td>
</tr>
<tr>
<td>insertsort</td>
<td>6573</td>
<td>10</td>
<td>41</td>
</tr>
<tr>
<td>loop3</td>
<td>13449</td>
<td>4</td>
<td>817</td>
</tr>
<tr>
<td>select</td>
<td>17088</td>
<td>15</td>
<td>151</td>
</tr>
<tr>
<td>qsort-exam</td>
<td>22146</td>
<td>15</td>
<td>170</td>
</tr>
<tr>
<td>fir</td>
<td>29160</td>
<td>9</td>
<td>105</td>
</tr>
<tr>
<td>sqrt</td>
<td>39962</td>
<td>14</td>
<td>477</td>
</tr>
<tr>
<td>ns</td>
<td>43319</td>
<td>13</td>
<td>64</td>
</tr>
<tr>
<td>qurt</td>
<td>214076</td>
<td>14</td>
<td>484</td>
</tr>
<tr>
<td>crc</td>
<td>290782</td>
<td>14</td>
<td>144</td>
</tr>
<tr>
<td>matmult</td>
<td>742585</td>
<td>23</td>
<td>100</td>
</tr>
<tr>
<td>bsort(100)</td>
<td>1567222</td>
<td>35</td>
<td>62</td>
</tr>
</tbody>
</table>

TABLE I
EXECUTION TIMES AND NUMBER OF UCBs AND ECBs FOR A SELECTION OF BENCHMARKS FROM THE MALARDALEN BENCHMARK SUITE.

Table II lists the breakdown utilization; the maximum utilization at which a scaled version of the case study taskset was deemed schedulable by each approach.

<table>
<thead>
<tr>
<th>Analysis</th>
<th>Breakdown utilization</th>
</tr>
</thead>
<tbody>
<tr>
<td>No Pre-emption Cost</td>
<td>0.95</td>
</tr>
<tr>
<td>Combined</td>
<td>0.767</td>
</tr>
<tr>
<td>ECB-Union</td>
<td>0.767</td>
</tr>
<tr>
<td>UCB-Only</td>
<td>0.75</td>
</tr>
<tr>
<td>UCB-Union</td>
<td>0.698</td>
</tr>
<tr>
<td>ECB-Only</td>
<td>0.612</td>
</tr>
<tr>
<td>Staschulat</td>
<td>0.508</td>
</tr>
</tbody>
</table>

TABLE II
CASE STUDY TASKSET: BREAKDOWN UTILIZATION FOR DIFFERENT APPROACHES.

Staschulat’s approach performs worst, with a breakdown utilization of 0.508. Equation (7) computes q that is the number of pre-emptions to be taken into account in the response time. For the effect of task t_1 (bs) to task t_3 (insertsort), only the 8 highest costs of t_1 pre-empting any task from t_2 to t_5 need to be considered. However, for the effect of task t_1 (bs) to task t_15 (bsort(100)), the 47362 highest costs need to be considered. Although the single pre-emption costs (for t_1 pre-empted by t_2) are much more precise, the total cost is very pessimistic.

The ECB-Union approach and the UCB-only approach perform best, with breakdown utilizations of 0.767 and 0.75. As the cache contention is high (3 out of the 15 tasks fill the whole cache), a single pre-emption often evicts all of the UCBs of the pre-empted task(s). In addition, the total number of ECBs is much higher than the total number of UCBs hence the ECB-only approach (3) is much more pessimistic than the UCB-only approach (4) and so has a lower breakdown utilization of 0.612. As a consequence, the ECB-Union ap-
proach (10) outperforms the UCB-Union approach (5) which has a breakdown utilization of 0.698. The combination of both approaches (11) does not improve upon the ECB-Union approach. Finally, (1) deems the case study taskset schedulable up to a utilization of 0.95 ignoring pre-emption costs.

VII. EVALUATION

In this section, we evaluate the effectiveness of the different approaches to pre-emption cost computation on a large number of tasksets with varying cache configurations and varying taskset parameters. The task parameters used in our experiments were randomly generated as follows:

- The default taskset size was 10.
- Task utilizations were generated using the UUUnifast [11] algorithm.
- Task periods were generated according to a log-uniform distribution with a factor of 100 difference between the minimum and maximum possible task period and a minimum period of 5ms. This represents a spread of task periods from 5ms to 500ms, as found in most automotive and aerospace hard real-time applications.
- Task execution times were set based on the utilization and period selected: \( C_i = U_i \cdot T_i \).
- Task deadlines were implicit\(^7\), i.e., \( D_i = T_i \).
- Priorities were assigned in deadline monotonic order.

The following parameters affecting pre-emption costs were also varied, with default values given in parentheses:

- The number of cache-sets \( (CS = 256) \).
- The block-reload time \( (BRT = 8\mu s) \).
- The cache usage of each task, and thus, the number of 
  ECBs, were generated using the UUUnifast [11] algorithm
  for a total cache utilization \( CU = 10 \). UUUnifast may
  produce values larger than 1 which means a task fills
  the whole cache. We assumed the ECBs of each task to
  be consecutively arranged starting at a random cache set
  \( S \in [0; CS - 1] \), i.e., from \( S \) to \( S + \left| ECB \right| \mod CS \).
- For each task, the UCBs were generated according to a
  uniform distribution ranging from 0 to the number of
  ECBs times a reuse factor: \( [0; RF \cdot \left| ECB \right|] \). The factor
  RF was used to adapt the assumed reuse of cache-sets
  to account for different types of real-time applications,
  for example, from data processing applications with little
  reuse up to control-based applications with heavy reuse.

Staschult’s approach exploits the fact that for the \( i \)-th pre-emption only the \( i \)-th highest number of UCBs has to be considered. As our case study and other measurements [10] have shown, a significant reduction typically only occurs at a high number of pre-emptions. For the purposes of evaluation, for Staschult’s approach, we simulated what in practice is likely to be an optimistic reduction: reducing the number of UCBs per pre-emption by one each time.

In each experiment the taskset utilization not including pre-emption cost was varied from 0.025 to 0.975 in steps of 0.025. For each utilization value, 1000 tasksets were generated and the schedulability of those tasksets determined using the appropriate pre-emption cost computation integrated into response time analysis.

A. Base configuration

We conducted experiments varying the number of tasks, the cache-size (i.e. number of cache-sets (CS)), the block reload time (BRT), the cache utilization (CU) and the reuse factor (RF). As a base configuration we used the default values of 10 tasks, a cache of 256 cache-sets, a block-reload time of 8µs, a reuse factor of 30% and a cache-utilization of 10. The latter two parameters were chosen according to the actual values observed in the case-study. Figure 7 illustrates the performance of the different approaches for this base configuration. The graph also shows a line marked Simulation-UB. This refers to the use of simulation to form a necessary schedulability test. We simulated execution and pre-emption of the tasks starting from near simultaneous release. (The tasks were released in order, lowest priority first, to increase the number of pre-emptions considered). If any task missed its deadline, then the taskset was proven to be unschedulable w.r.t. the pre-emption cost model used\(^4\), thus providing a valid upper bound on taskset schedulability including pre-emption costs. Note that the lines on the graphs appear in the same order as they are described in the legend. The graphs are best viewed online in colour.

![Fig. 7. Evaluation of base configuration. Number of tasksets deemed schedulable at the different total utilizations.](image)

For each approach, we determined the average breakdown utilization for the tasksets generated for the base configuration, see Table III. These results show that the ECB-Union, and

\(^7\)Evaluation for constrained deadlines, i.e., \( D_i \in [2C_i; T_i] \) gives broadly similar results although fewer tasksets are deemed schedulable by all approaches, see the Appendix of [3] for further details.

\(^4\)The simulation assumed that any partial execution of a task uses all its ECBs and UCBs.
Combined approaches significantly improve upon the performance of previous methods.

<table>
<thead>
<tr>
<th>Analysis</th>
<th>Average Breakdown Utilization</th>
</tr>
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<tr>
<td>No Pre-emption Cost</td>
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<tr>
<td>Combined</td>
<td>0.64</td>
</tr>
<tr>
<td>ECB-Union</td>
<td>0.62</td>
</tr>
<tr>
<td>UCB-Union</td>
<td>0.57</td>
</tr>
<tr>
<td>UCB-Only</td>
<td>0.55</td>
</tr>
<tr>
<td>ECB-Only</td>
<td>0.39</td>
</tr>
<tr>
<td>Staschulat</td>
<td>0.35</td>
</tr>
</tbody>
</table>

**TABLE III**

Average breakdown utilization of base configuration tasks sets.

Exhaustive evaluation of all combinations of cache and taskset configuration parameters is not possible. We therefore fixed all parameters except one and varied the remaining parameter in order to see how performance depends on this value. The graphs below show the weighted schedulability measure \( W_y(p) \) [8] for schedulability test \( y \) as a function of parameter \( p \). For each value of \( p \), this measure combines data for all of the tasks sets \( r \) generated for all of a set of equally spaced utilization levels. Let \( S_y(r, p) \) be the binary result (1 or 0) of schedulability test \( y \) for a taskset \( r \) and parameter value \( p \) then:

\[
W_y(p) = \left( \sum_{r} u(r) \cdot S_y(r, p) \right) / \sum_{r} u(r)
\]

where \( u(r) \) is the utilization of taskset \( r \). This weighted schedulability measure reduces what would otherwise be a 3-dimensional plot to 2 dimensions [8]. Weighting the individual schedulability results by taskset utilization reflects the higher value placed on being able to schedule higher utilization tasksets.

**B. Cache Utilization & Cache-Reuse**

Cache utilization and cache-reuse are the most important factors for pre-emptively scheduled systems. If all tasks fit into the cache, i.e. the cache utilization is less than one or there is no cache-reuse at all, then no additional cache-related pre-emption delays occur. The other extreme is when each task completely fills the cache. In this case, each UCB must be assumed to be evicted, and hence the overall pre-emption delay depends solely on the number of UCBs.

Figure 8 shows the weighted schedulability measure for each approach as a function of the cache utilization. At a low cache utilization, only a few UCBs are actually evicted. The set of ECBs per task is low, and often smaller than the number of UCBs of all possibly pre-empted tasks. Thus, an upper bound on the possibly evicted UCBs per pre-empting task (as computed by the UCB-Union approach) is slightly pessimistic, while the ECB-Union approach is in this case more pessimistic. The situation changes with increased cache utilization. As each task uses a larger proportion of the whole cache on average, the UCB-Union approach becomes significantly more pessimistic than the ECB-Union approach.

Figure 9 shows the weighted schedulability measure for each approach as a function of the reuse factor. At low values of the reuse factor, the set of UCBs per task is low compared to the ECBs, and so the UCB-Union approach is more pessimistic than the ECB-Union approach, while at high values of the reuse factor, the opposite applies as the set of UCBs for each task becomes similar to its set of ECBs. Observe that in Figure 9 these two lines cross at a medium level of reuse, while the Combined approach outperforms both, providing the best performance in all cases. Since the reuse factor only affects the number of UCBs, the performance of the ECB-only approach is independent of the reuse factor. As expected, performance of the ECB-only approach is relatively poor at low levels of reuse, but competitive at high levels.
C. Number of Tasks

In this experiment, we varied the number of tasks with the other parameters fixed at their default values. Figure 10 shows that the more tasks there are, the less likely a taskset of a given utilization is to be schedulable. This is because with an increased number of tasks the number of pre-emption and hence the overall pre-emption costs increase, reducing the schedulability of the taskset. This reduction in schedulability with increasing taskset size holds for all of the approaches, with a greater reduction observed with Staschulat’s approach for the reasons explained in Section VI.

Note that the upper bound derived by simulation shows a much smaller reduction. This is because, as the number of tasks increases, the number of possible execution scenarios increases rapidly, thus it becomes less likely that the simulation will deliver the worst-case scenario.

D. Cache-Size

The number of cache-sets also has an influence on the overall performance of the different approaches. Given the same cache utilization and block reload time, the more cache-sets there are, the higher the impact of a pre-emption may be. Hence as the number of cache sets is increased, all of the approaches show a similar decrease in schedulability with the exception of the basic response time analysis which does not include pre-emption costs, see Figure 11.

Varying the block reload time results in similar behaviour, see Figure 12.

We note that when increasing the cache size, the execution time of each task might also be expected to decrease. In this experiment, however, we keep WCET’s constant and examine only the effect on schedulability of changing the cache size.

The pre-emption costs are not included in the taskset utilization.

VIII. CONCLUSIONS

The major contribution of this paper is the introduction of a new method of bounding pre-emption costs, called the ECB-Union approach. This approach dominates the UCB-Only approach of Lee [18]. The ECB-Union approach complements the UCB-Union approach of Tan and Mooney [26], which dominates the ECB-only approach of Busquets and Wellings [13] and Tomiyama and Dutt [27]. The ECB-Union and UCB-Union approaches are incomparable and so we combined them into a composite response time test that dominates the use of either approach on its own.

We extended the ECB-Union and UCB-Union approaches to systems that permit tasks to access shared resources in mutual exclusion according to the Stack Resource Policy. Our work in this area revealed that previous approaches to computing pre-emption delays, although including blocking factors in their schedulability analyses, did not account for the pre-emption
of blocking tasks during a resource access. This omission can lead to optimistic (unsound) response times, an issue that we corrected.

Finally, we examined the performance of the various approaches to computing pre-emption costs via a case study and an empirical evaluation of taskset schedulability. The latter showed that a combined response time analysis test using both the new ECB-Union approach derived in this paper, and the UCB-Union approach of Tan and Mooney [26] provides an effective method of determining task schedulability. This combined approach offers a significant improvement in performance over previous approaches for a wide range of different task and cache configurations, including cache utilization level, amount of reuse, cache size, and block reload times.

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References